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&his core is inten+e+ to be use+ as an interface bet) een an external mixrocontroller an+ an ,P- (! Within that ,P- ( several functions are embe++e+ that are controlle+ b the external microcontroller! & picall the microcontroller +oes not have a regular memor or I. / bus to interface to external functions! &herefore a simple solution using a parallel port is use+ here!

&he WIS0 / 1E specification an+ a++itional information agas isIS0! The hebt21 Probe uuco+ a R> ognh by s& c0032 dee E

## External Parallel Port to Internal Wishbone Interface

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2ommunication bet ) een microcontroller an+ , P - ( uses a simple parallel port! 8 icrocontroller is bus master!

! " # Write 2 cle3

